

Electronic Patent Application Fee Transmittal

Application Number:	10751324			
Filing Date:	05-Jan-2004			
Title of Invention:	INTEGRATED CIRCUITS WITH RAM AND ROM FABRICATION OPTIONS			
First Named Inventor/Applicant Name:	Raminda Udaya Madurawe			
Filer:	Jose S. Garcia/Donna Petford			
Attorney Docket Number:				
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Maintenance Fee Due at 3.5 years	1551	1	980	980
Surcharge after Expiration-Unintentional	1558	1	1640	1640
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				2620